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FIG. 1

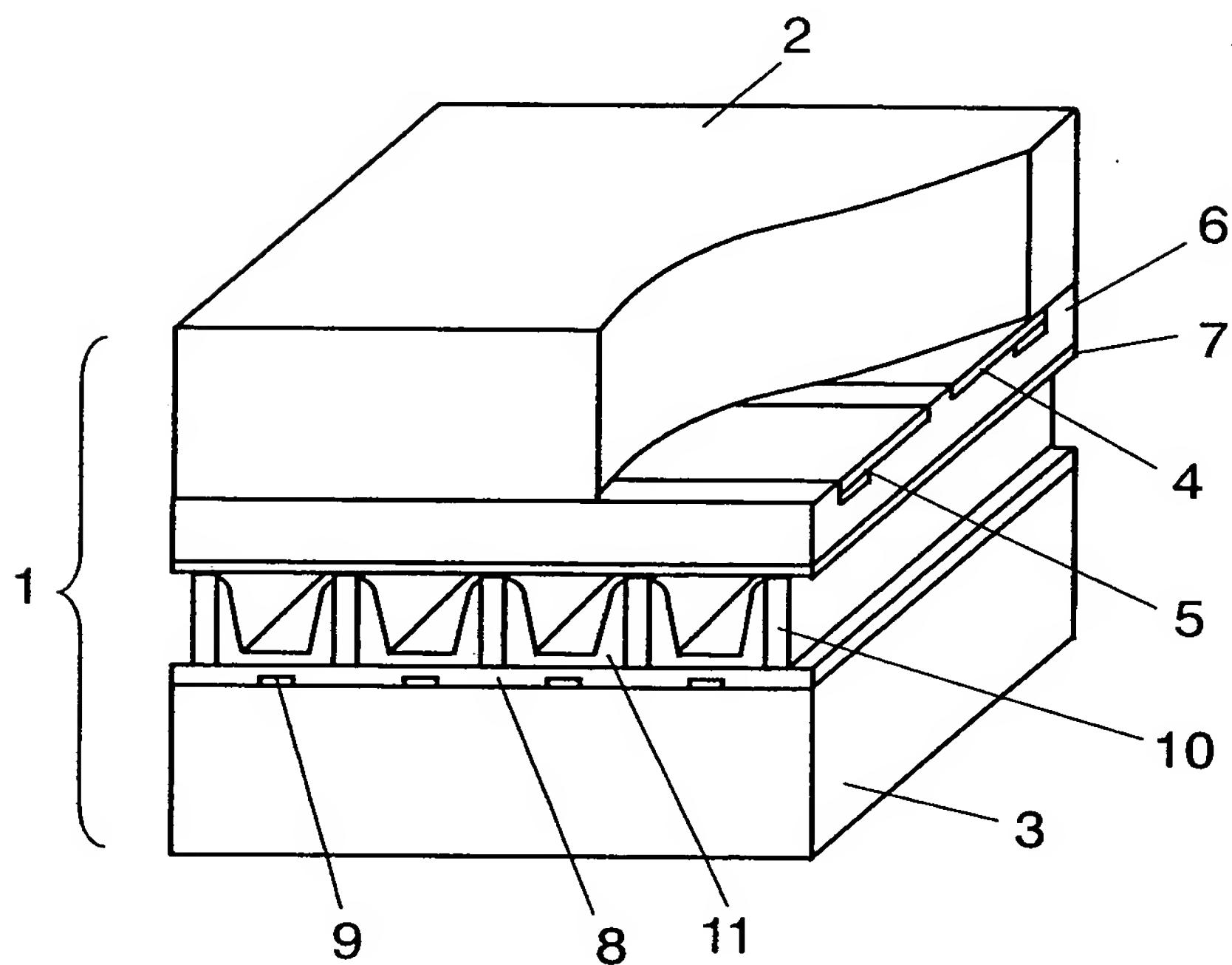
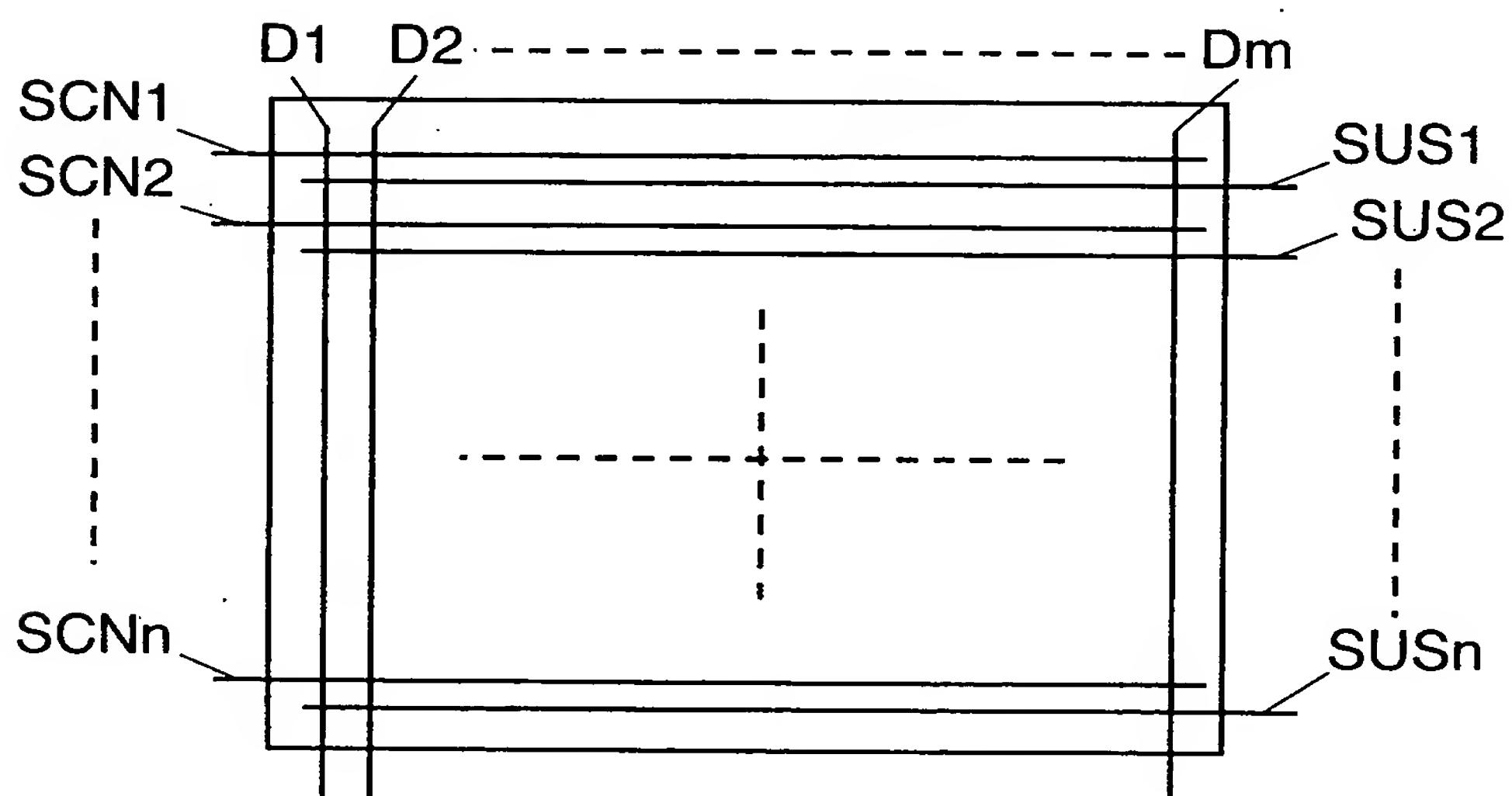


FIG. 2



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FIG. 3

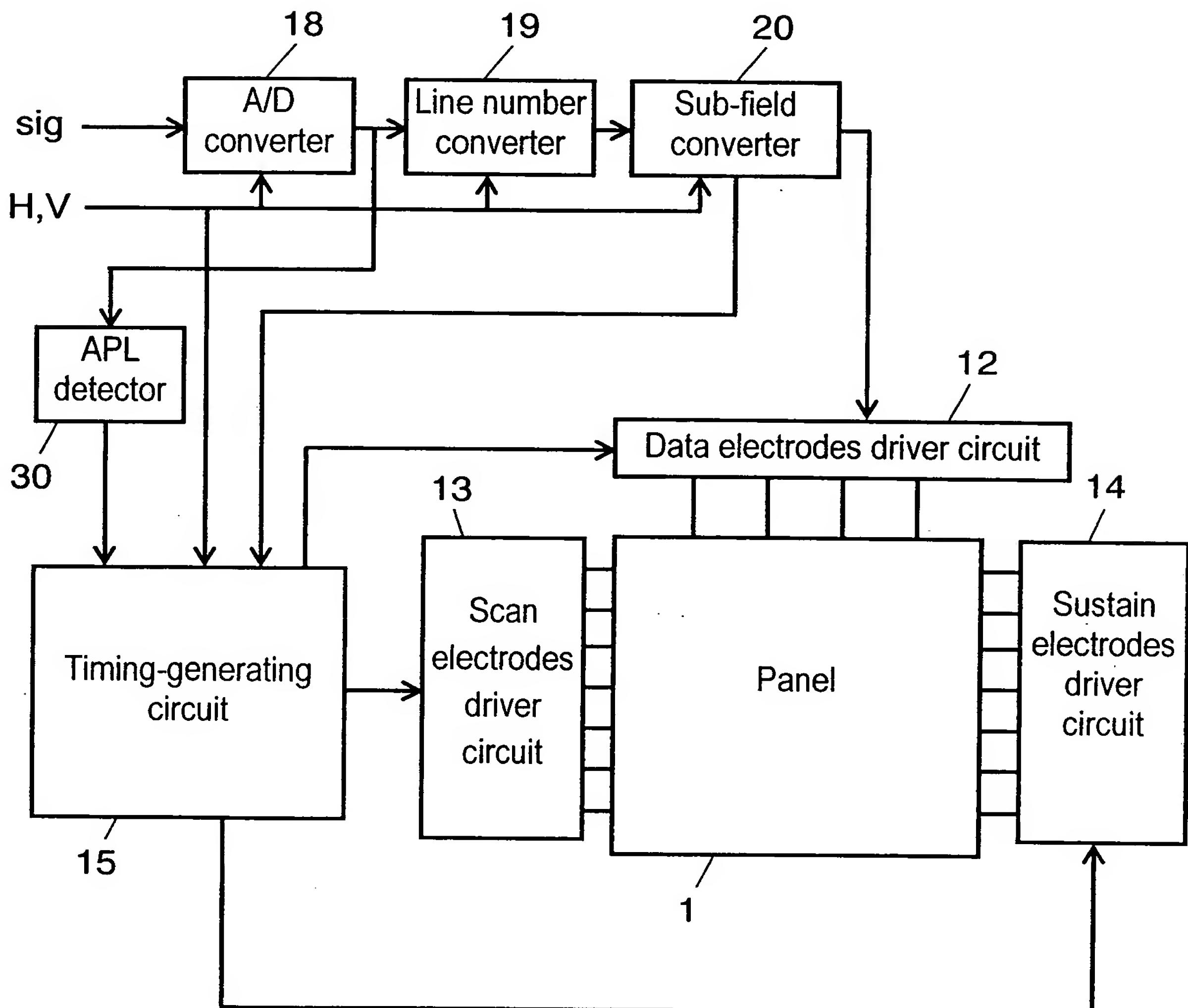
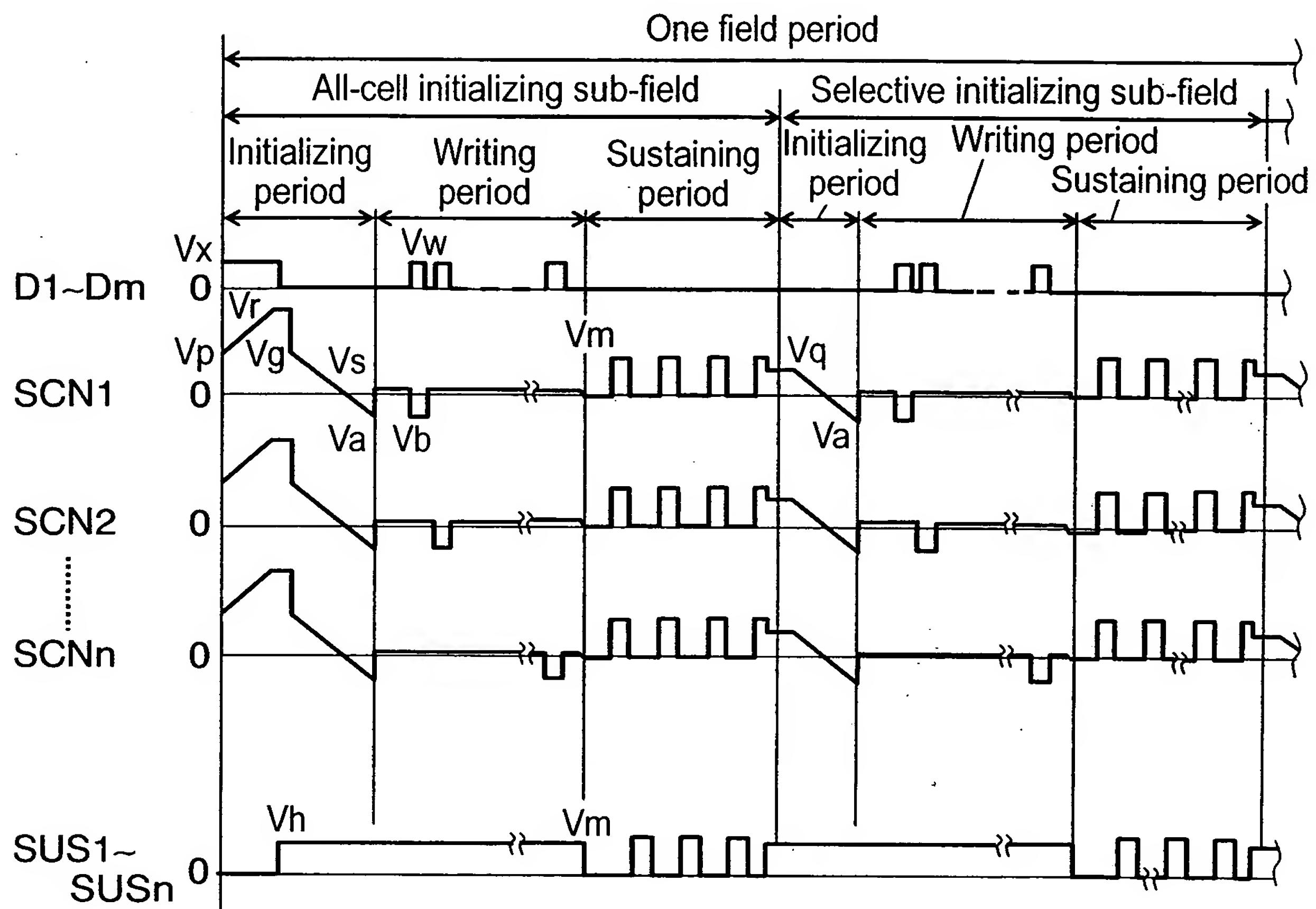


FIG. 4

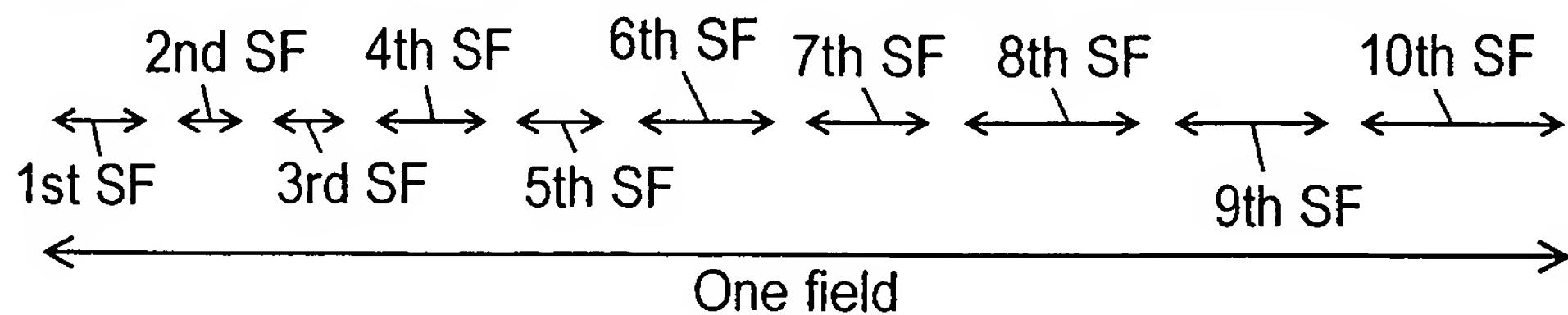
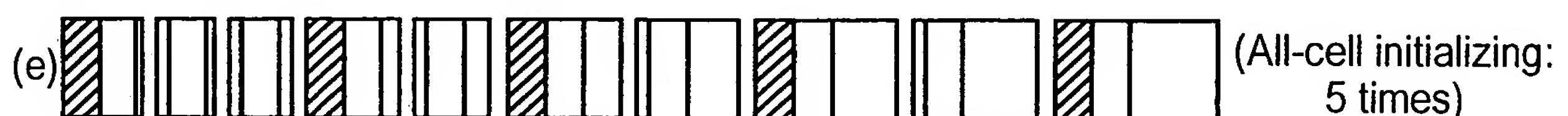
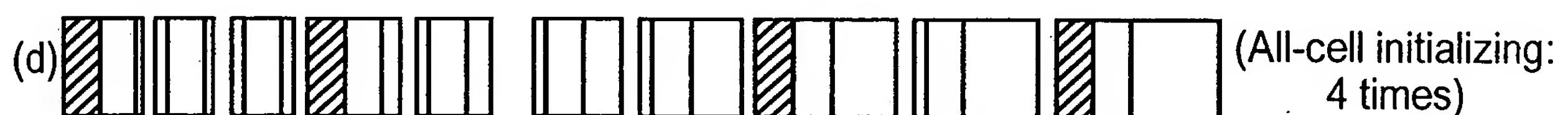
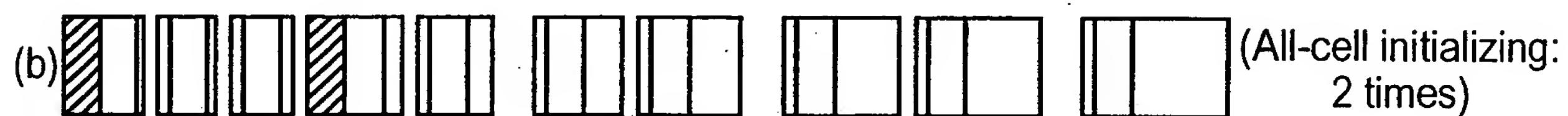
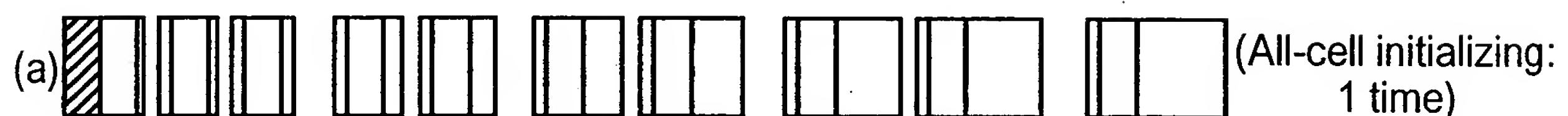
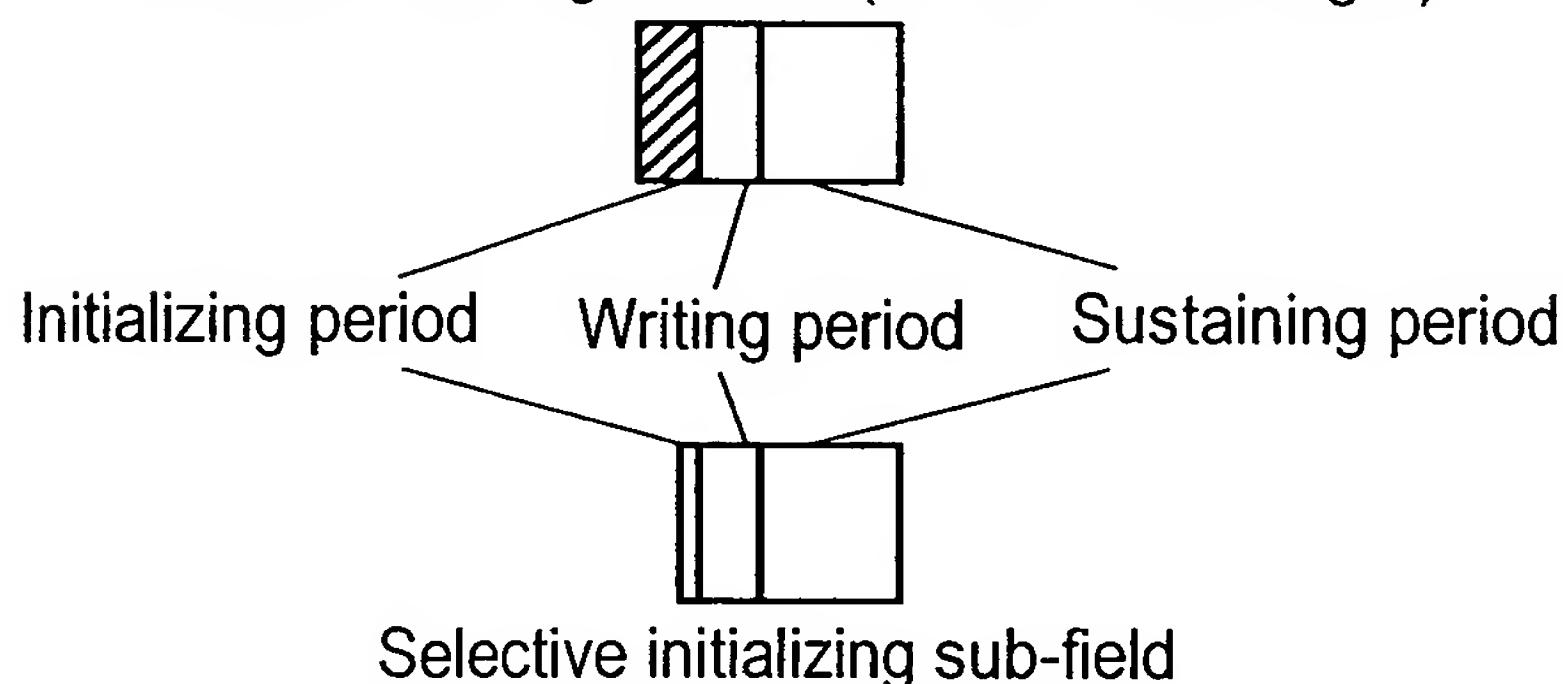


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FIG. 5

All-cell initializing sub-field(All-cell initializing A)



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Reference numerals in the drawings

- 1 Panel
- 2 Front substrate
- 3 Rear substrate
- 4 Scan electrode
- 5 Sustain electrode
- 9 Data electrode
- 15 Timing-generating circuit
- 30 APL detector